

**METHOD AND ARRANGEMENT FOR DETECTING AND CORRECTING LINE  
DEFECTS**

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Abstract

A detection and correction method for detecting and  
correcting line defects, as well as a circuit  
arrangement, are provided to execute a checking routine  
on every single line, whereby all errors are reliably  
10 detected. By virtue of the detection, only one additional  
fallback line need be provided for each single line error  
to be corrected, to which fallback line a switchover is  
made in the event of an error.